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Number of electrodes	64×64
Electrode size	$21 \; \mu m \times 21 \; \mu m$
Electrode separation	$21 \; \mu m$
Active area	$2.67 \; mm \times 2.67 \; mm$
Chip power consumption	$132 \; mW$
Input-referred noise	$11 \; \mu V_{rms}$
Minimum sampling rate (4096 electrodes)	$7.8 \; kHz$
Maximum sampling rate (64 electrodes)	$125 \; kHz$
ADC resolution	$12 \; bits$
Data rate	$0.5 \; Gbit/s$

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